

Applicants:

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Vincent

Assignee:

Amkor Technology, Inc.

Title:

Semiconductor Package And Method For Fabricating The Same

Serial No.:

09/574,541

Filing Date:

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Examiner:

Not Yet Assigned

Group Art Unit:

2825

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COMMISSIONER FOR PATENTS

Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
 - 2. a representation that a search has been made, other than as described above; or
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

LAW OFFICES OF SKJERVEN MORRILL MacPHERSON LLP

25 METRO DRIVE SUITE 700 SAN JOSE, CA 95110 (408) 453-9200 FAX (408) 453-7979 Applicants submit that no fee is required. If, however, a fee is necessary under 37 R. 1.97(c), then such fee and any other appropriate fee may be charged to Deposit Account No. 19-2386.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Satents, Washington, D.C. 20231, on ________, 2001.

Attorney for Applicants

OCT 1

Date of Signature

Respectfully submitted,

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